

FIG. 1

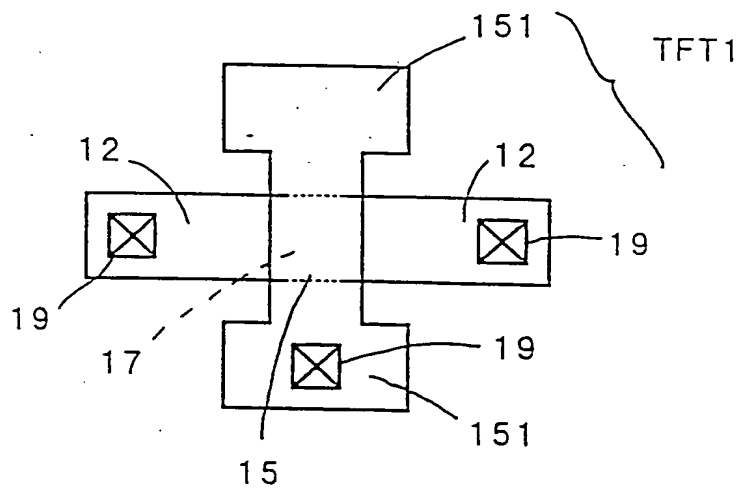


FIG. 2

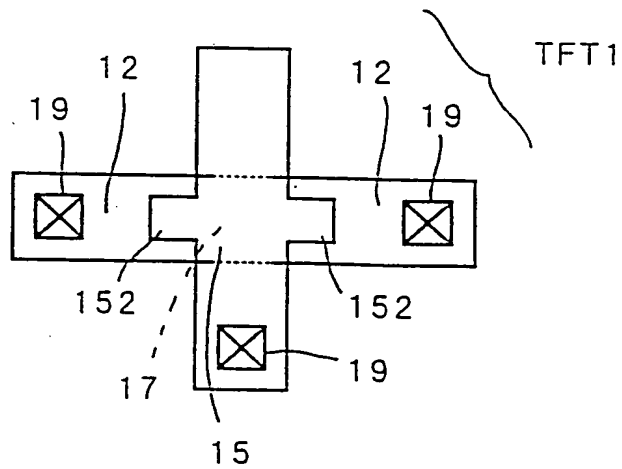


FIG. 3

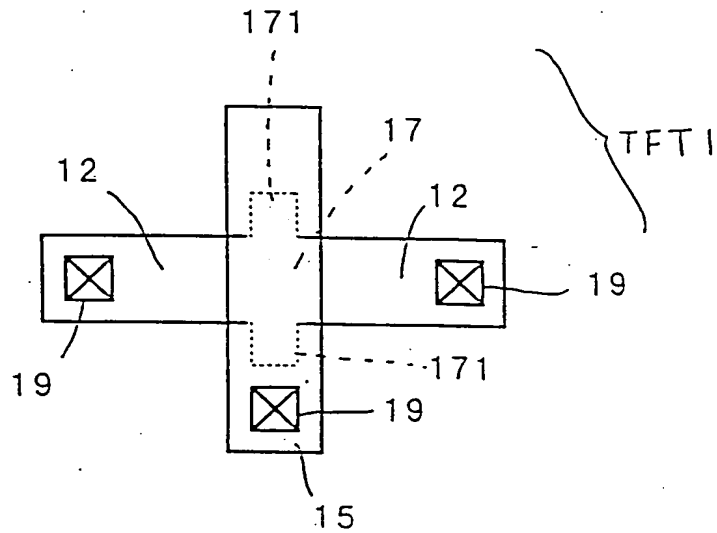
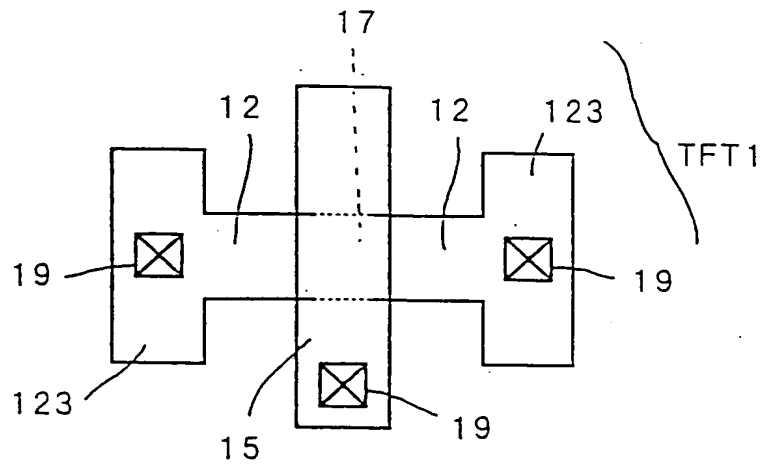


FIG. 4



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FIG. 5

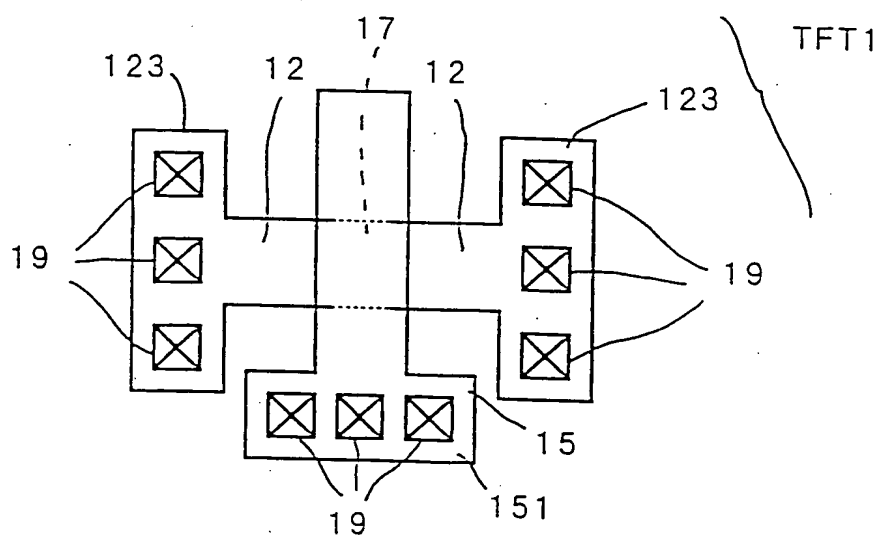


FIG. 6

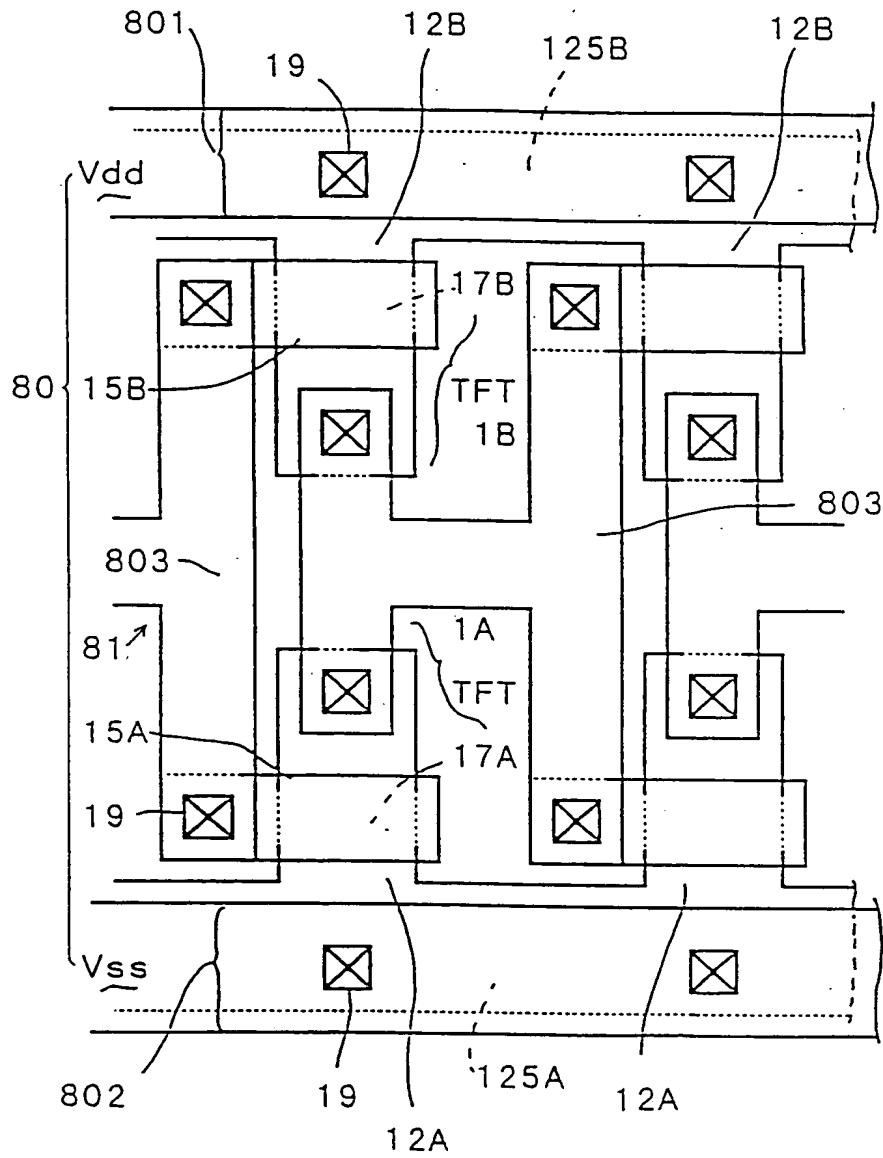
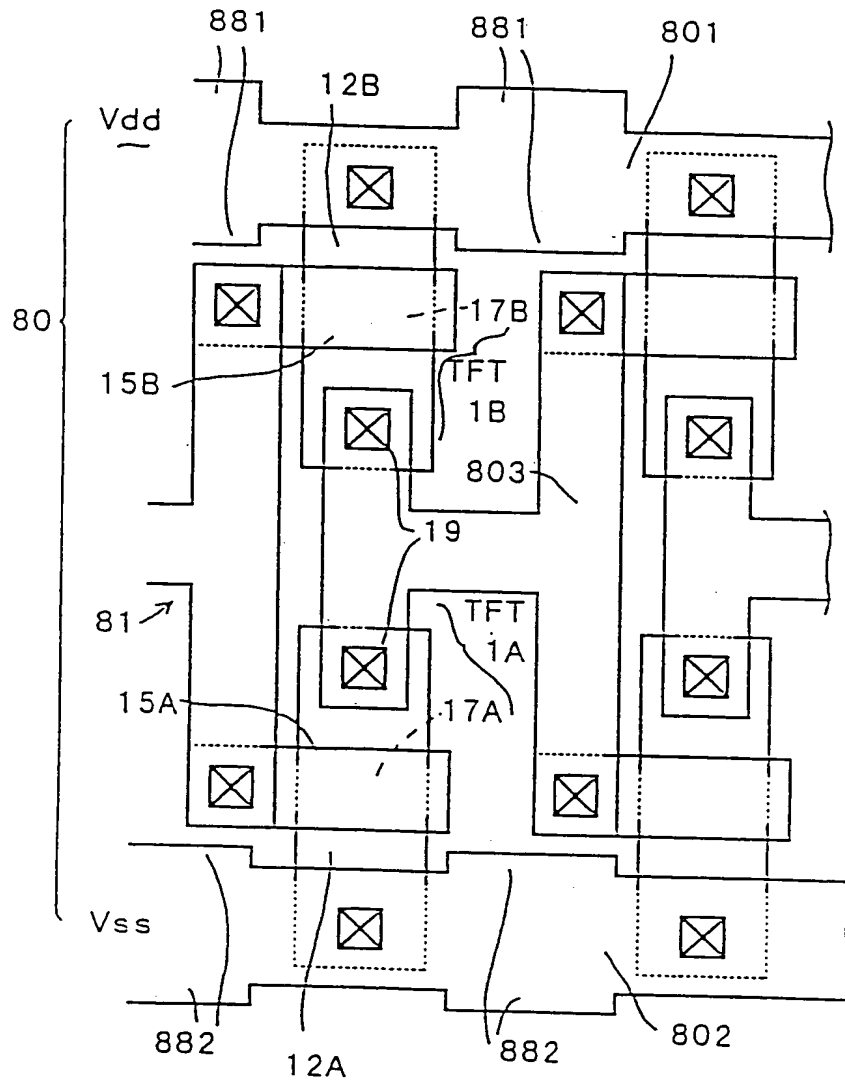


FIG. 7

(A)



(B)

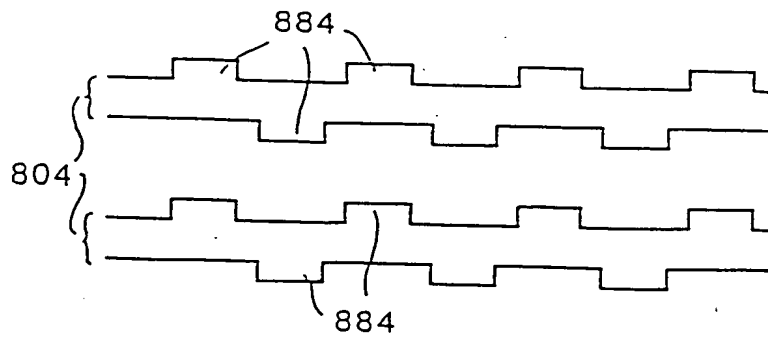


FIG. 8

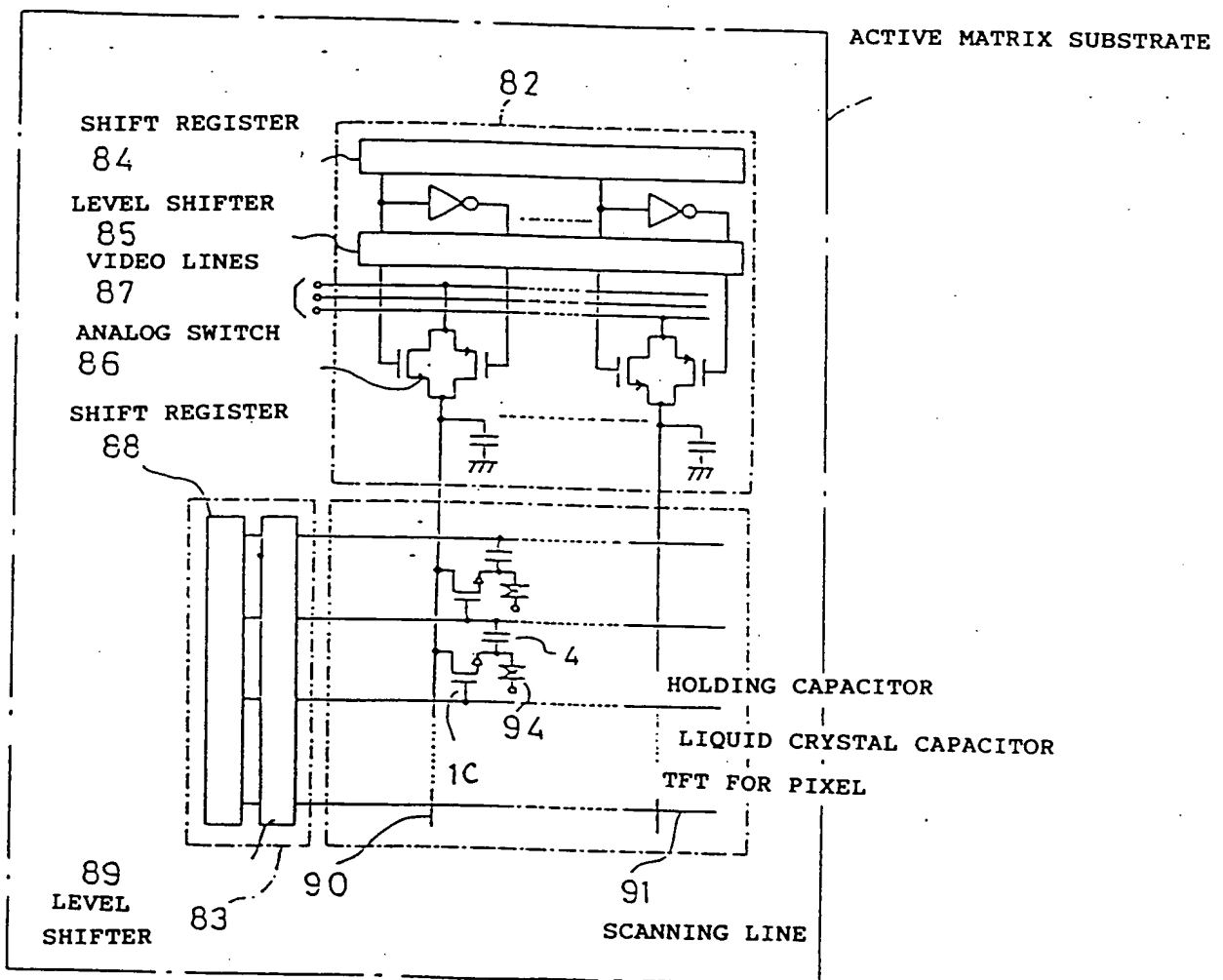
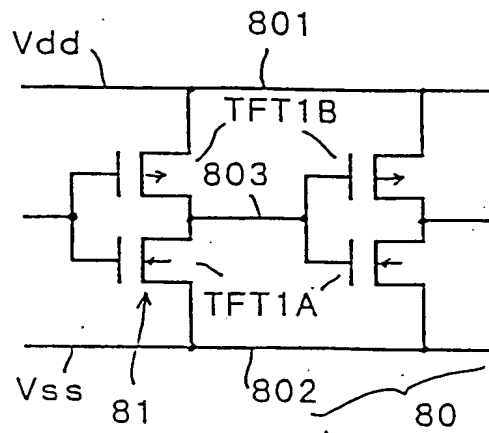
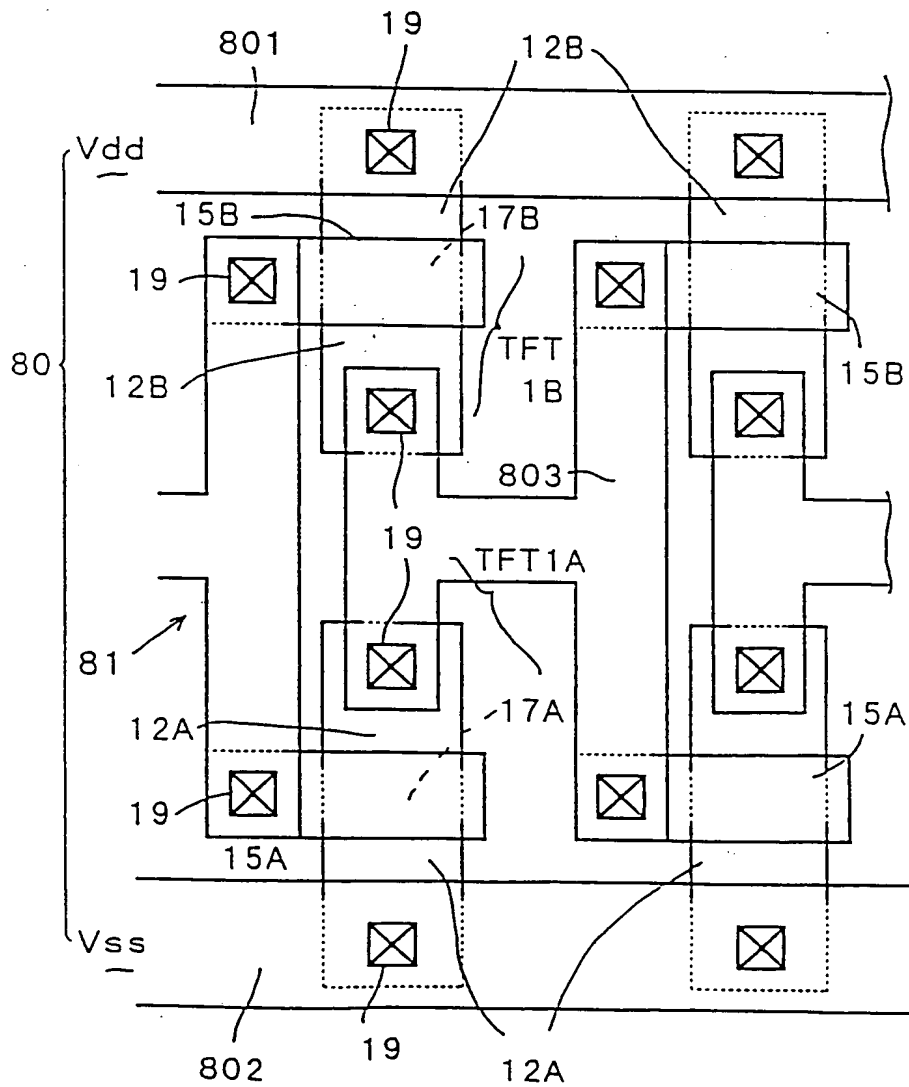


FIG. 9

(A)



(B)



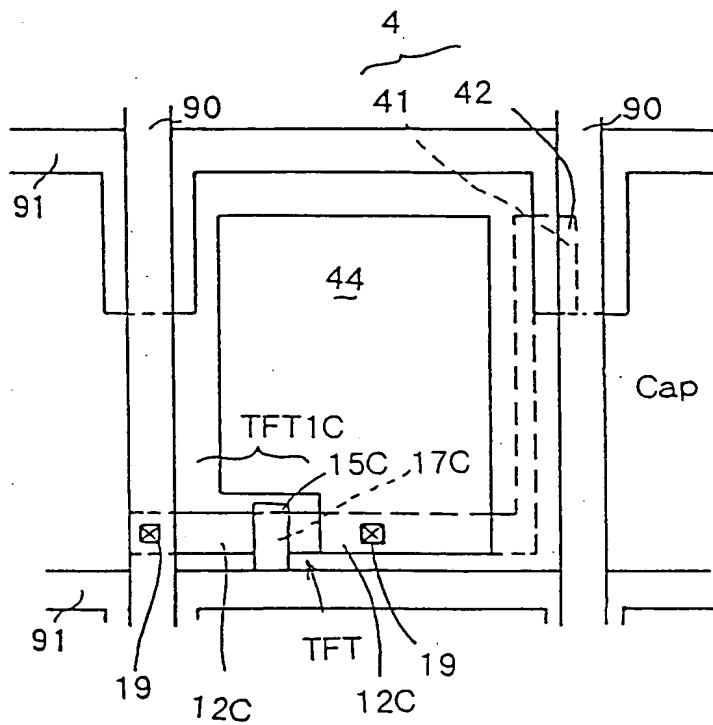
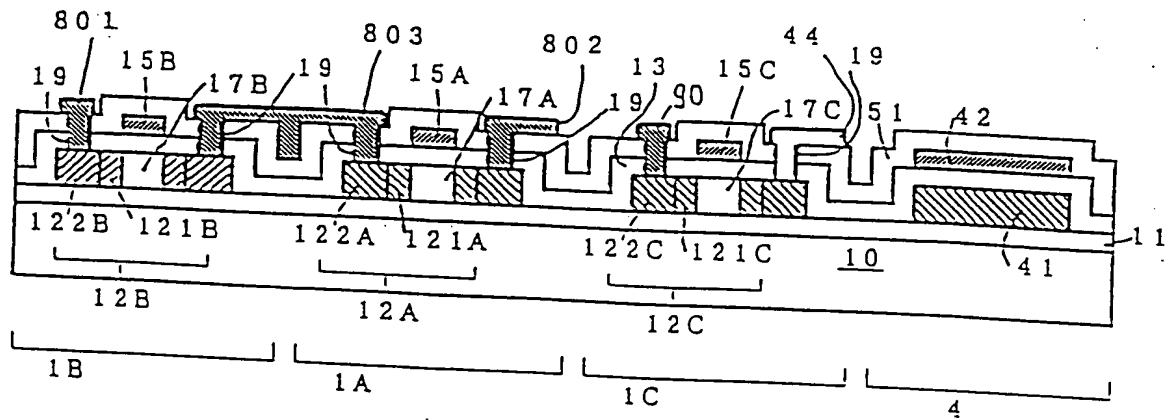


FIG. 11



P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

TFT FOR PIXEL

HOLDING CAPACITOR

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FIG. 12

LASER ANNEALING

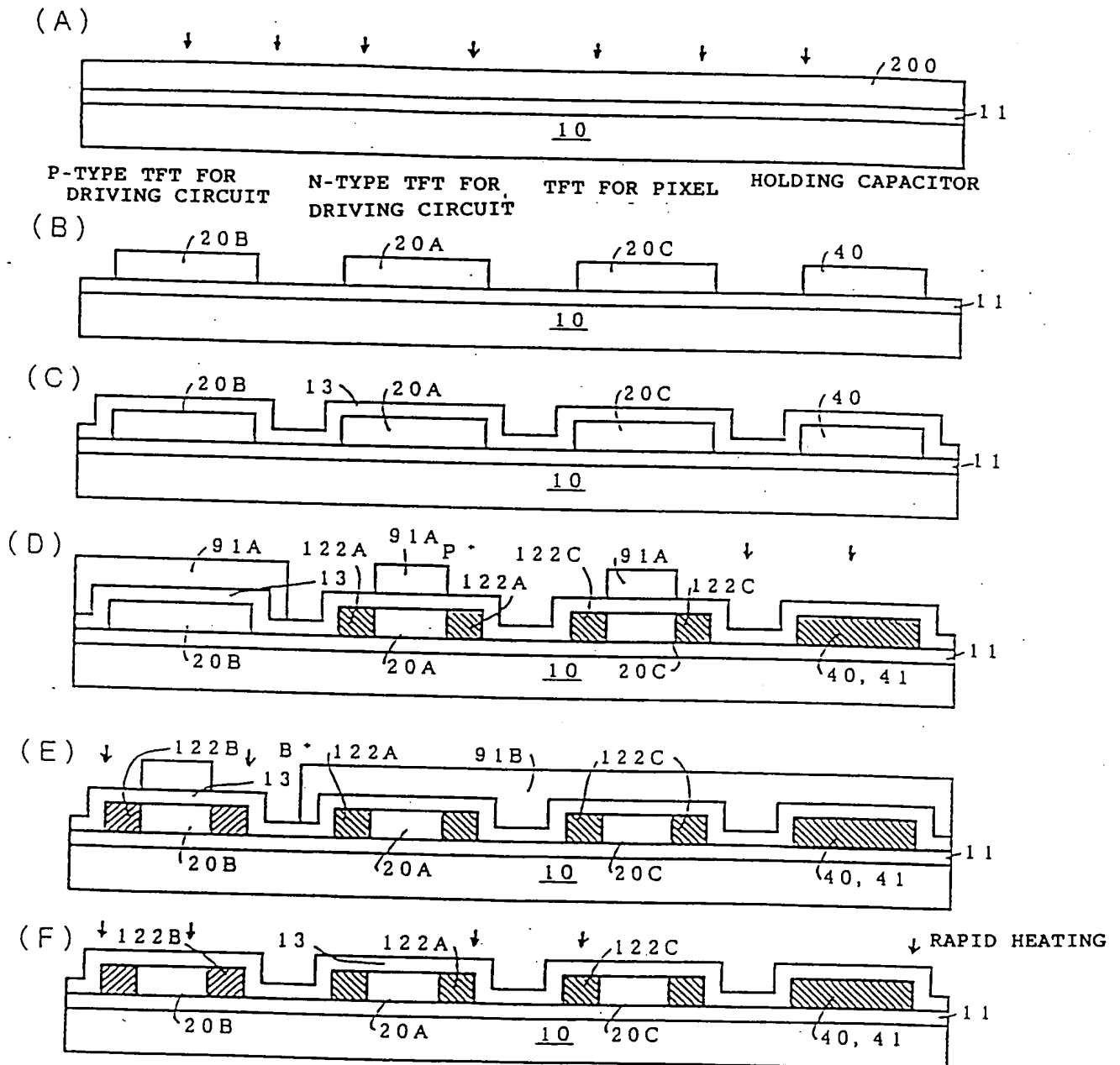
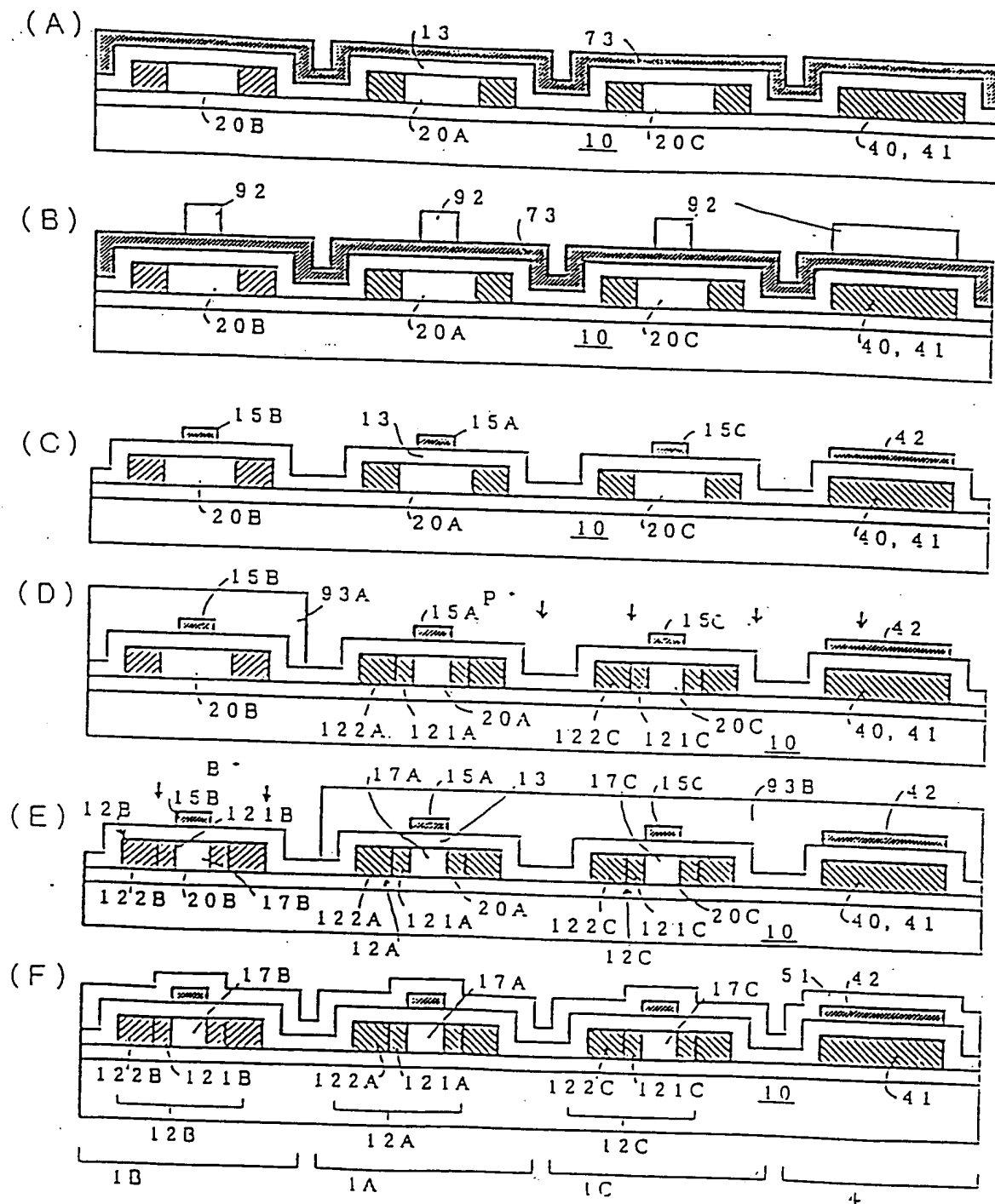


FIG. 13

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P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

TFT FOR PIXEL

HOLDING CAPACITOR

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FIG. 14

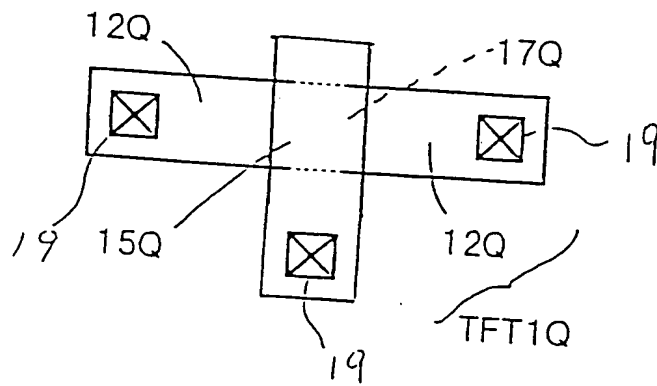
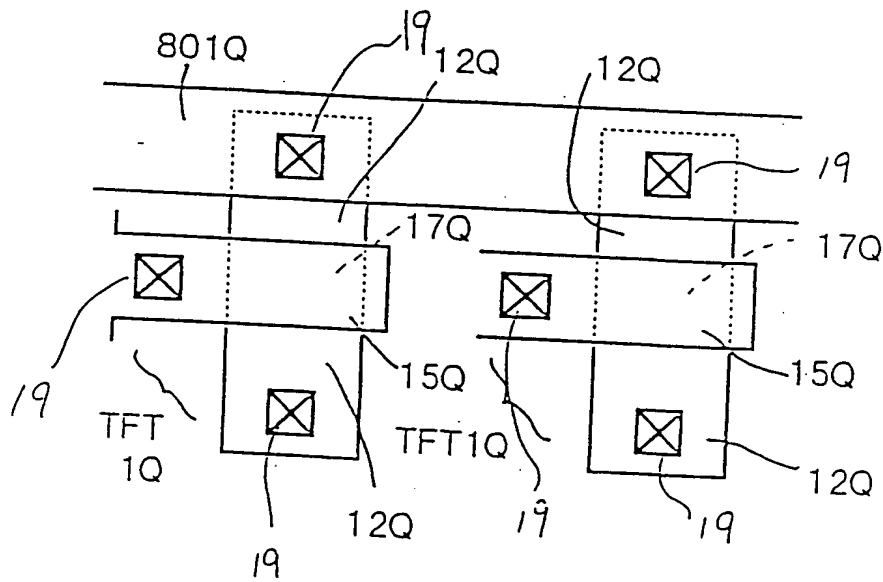


FIG. 15



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FIG. 16

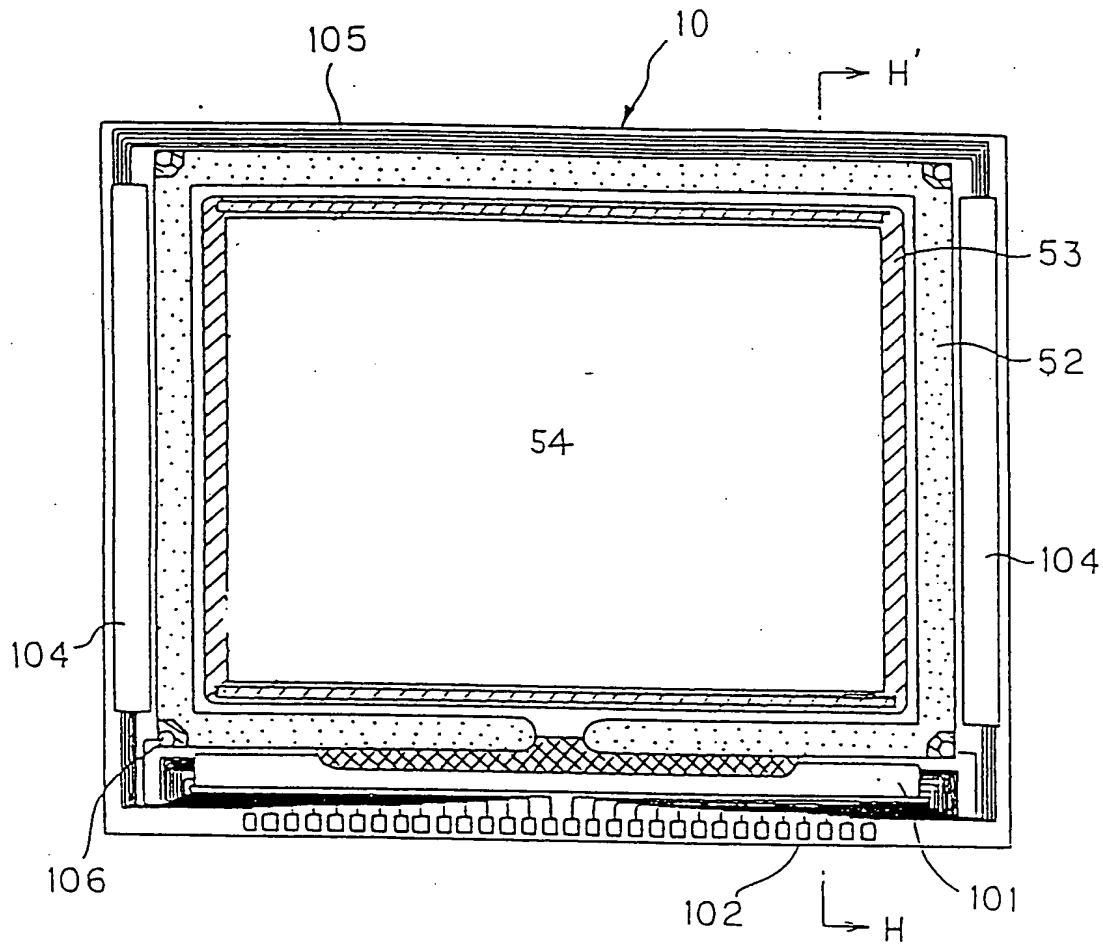
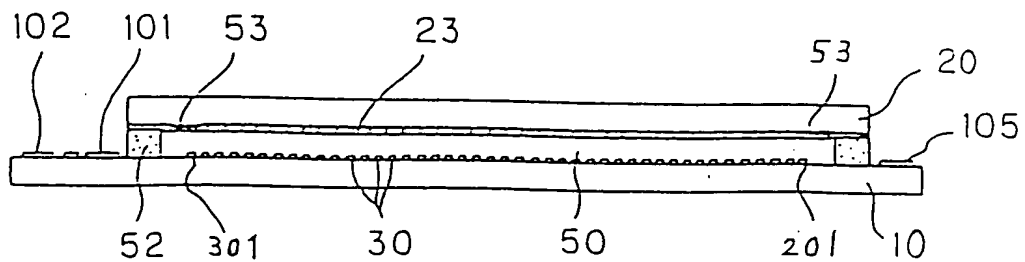


FIG. 17



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FIG. 18

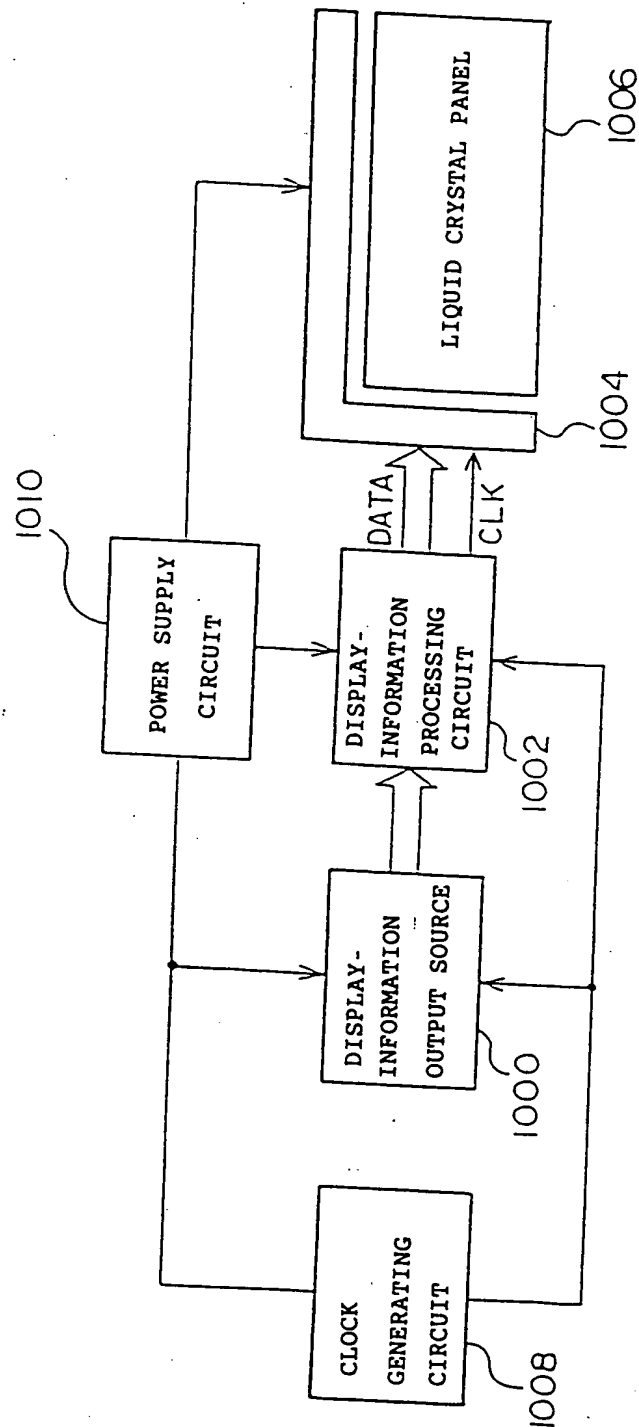


FIG. 19

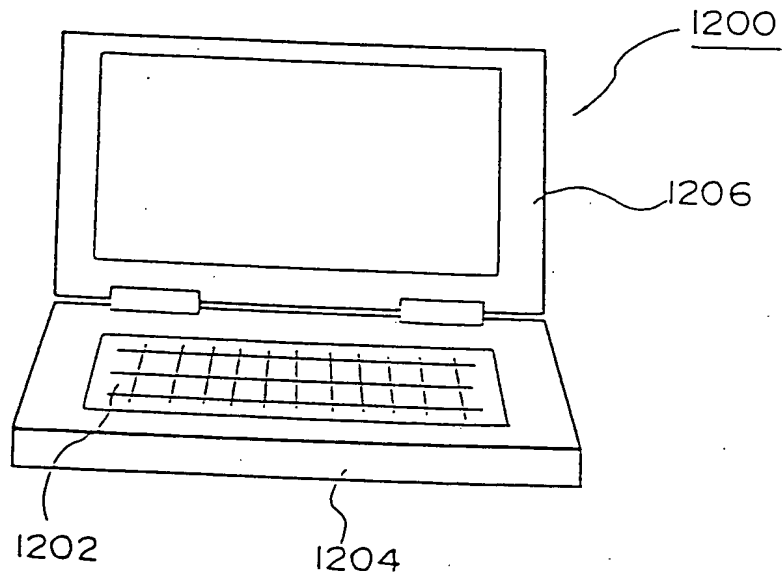


FIG. 20

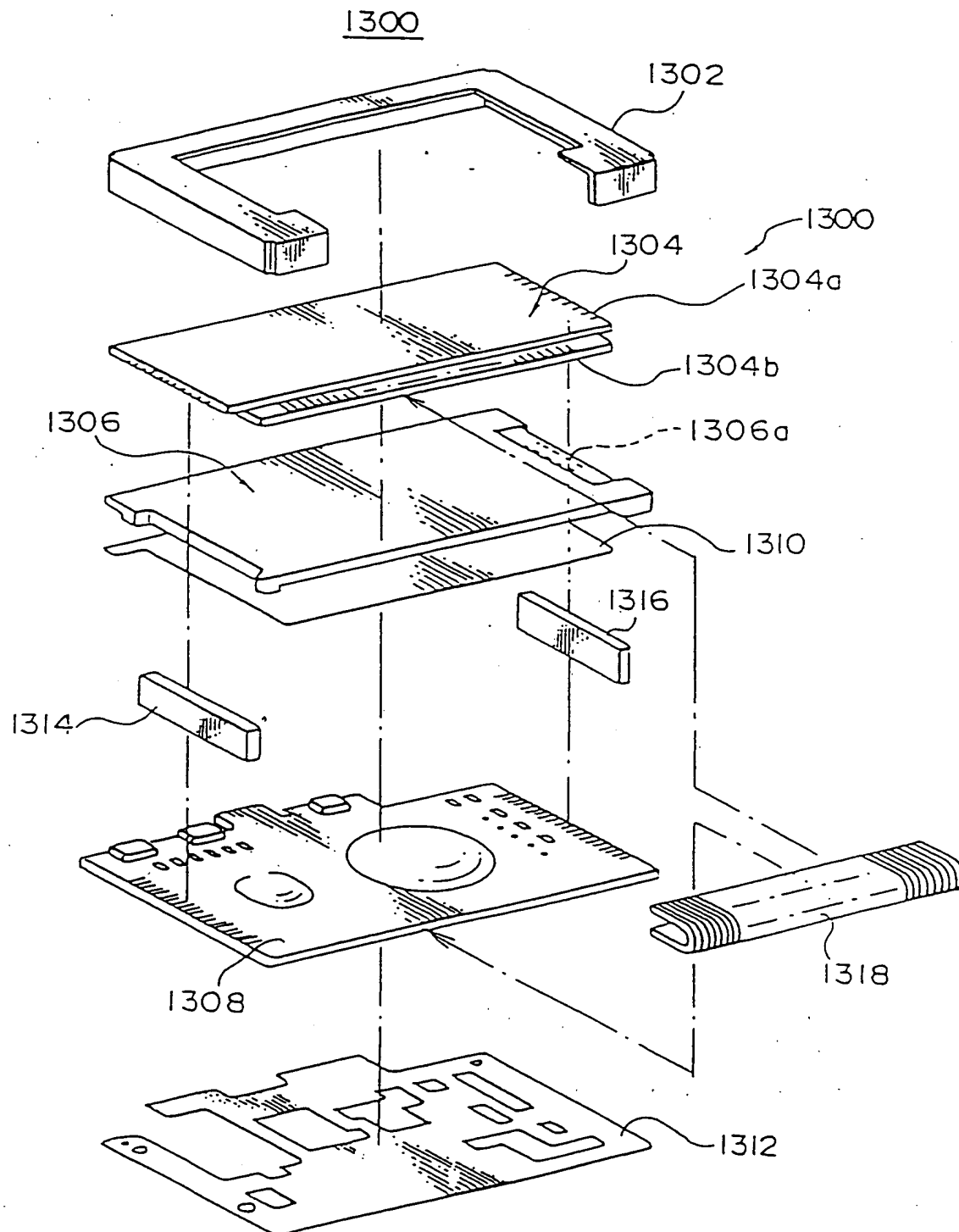


FIG. 21

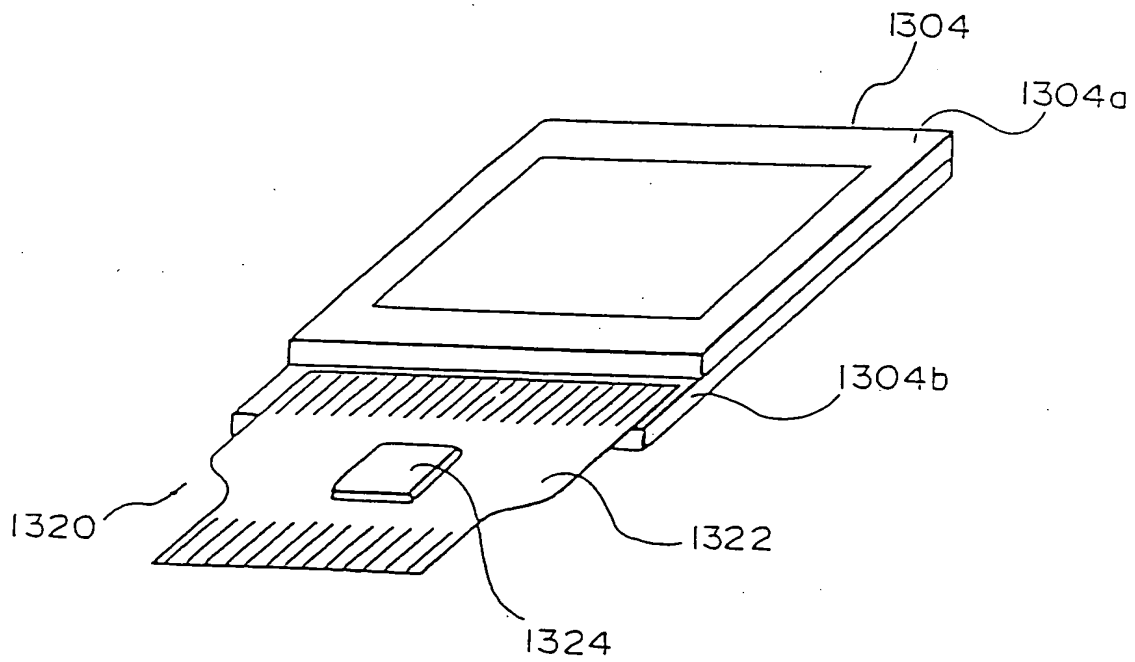


FIG. 22

